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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/733,343	12/12/2003	Jerome Cornet	ALC 3108	7458

7590 03/20/2007  
KRAMER & AMADO, P.C.  
Suite 240  
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Alexandria, VA 22314

EXAMINER

COLEMAN, ERIC

ART UNIT	PAPER NUMBER
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2183

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	03/20/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

**Office Action Summary**

Application No.

10/733,343

Applicant(s)

CORNET ET AL.

Examiner

Eric Coleman

Art Unit

2183

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 27 December 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-11, 15 and 16 is/are allowed.
- 6) ☒ Claim(s) 12-14 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- ☒ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_.
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_.
- ☐ Notice of Informal Patent Application
- ☐ Other: \_\_\_\_\_.

## **DETAILED ACTION**

### ***Claim Rejections - 35 USC § 112***

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claim 12-14 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 12 has the language "loge". Therefore the scope of meaning of claim 12 is not clear.

Claim 13 does not end in a period and therefore the scope of meaning of claim 13 (and claim 14) is not clear.

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 12 is rejected under 35 U.S.C. 103(a) as being unpatentable over Connor (patent No. 5,553,082) in view of Nakamura (patent No. 6,920,590).

Connor taught the invention substantially as claimed including a data processing ("DP") system comprising:

Processor (e.g. see figs. 1,4,6) for use in a pattern matching engine, processor having combinatory logic means (e.g., see fig. 2) for receiving outputs from pattern matching RAM (12); a register (32) (e.g., see figs. 1,4,6) and a AND Gate (42) (e.g., see fig. 2) combining the outputs from the respective logic [understood as "logic:"] means to output an indication of a matched pattern ( e.g., see fig. 2, and col. 5, , line 21-col. 6, line 61).

Connor did not expressly detail plural RAMS. Nakamura however taught pattern matching using plural RAMS (22a,22b) (e.g., see fig. 5 and col. col. 8, line 47-65).

It would have been obvious to one of ordinary skill in the DP art to combine the teachings of Connor and Nakamura. Both references were directed toward the problems of testing random access memory in a DP system. One ordinary skill would have been motivated to incorporate the Nakamura teachings of plural RAMs coupled to pattern matching means at least to reduce the time required to test the semiconductor apparatus (e.g., see col. 2, lines 5-13 of Nakamura).

#### ***Allowable Subject Matter***

Claims 1-11, and 15-16 are allowed.

#### ***Response to Arguments***

Applicant's arguments with respect to claim 12-14 have been considered but are moot in view of the new ground(s) of rejection.

#### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Kraus (patent No. 6,587,979) disclosed a partitionable embedded circuit test system for integrated circuit (e.g., see abstract).

Takano (patent No. 6,097,206) disclosed a memory tester switching the tester to RAM test mode and ROM test mode (e.g., see abstract).

Byers (patent No. 5,475,815) disclosed a built-in test scheme for testing multiple memory elements (e.g., see abstract).

Menon (patent No. 5,703,964) disclosed a pattern recognition system with statistical classification (e.g., see abstract).

Johnsen (patent No. 4,606,069) disclosed a system for compression of facsimile information by pattern matching (e.g., see abstract).

Werth (patent No. 4,551,850) disclosed a system with response detector for pattern processing system (e.g., see abstract).

Schwarz (patent No. 5,982,681) disclosed a reconfigurable built-in self test circuit (e.g., see abstract).

Murai (patent No. 5,521,921) disclosed a data communications system with frame analyzer (e.g., see abstract).

Allison (patent application publication No. 20020077995 A1) disclosed a pattern matching in a communications network (e.g., see abstract).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Eric Coleman whose telephone number is (571) 272-4163. The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Chan can be reached on (571) 272-4162. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

EC

A handwritten signature in black ink, appearing to read 'Eric Coleman', with a stylized flourish at the end.

**ERIC COLEMAN**  
**PRIMARY EXAMINER**